

# Enhancement Mode N-Channel Power MOSFET

## Features

- ◆ Low  $R_{DS(on)}$  & FOM
- ◆ Extremely low switching loss
- ◆ Excellent stability and uniformity
- ◆ Easy to drive

## Applications

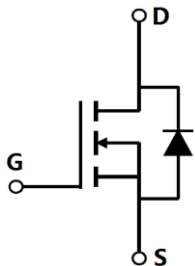
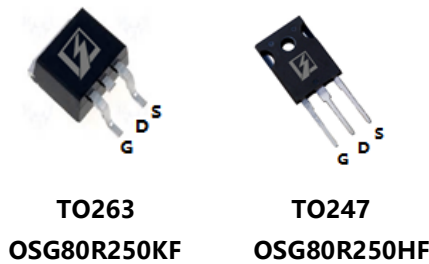
- ◆ Lighting
- ◆ Hard switching PWM
- ◆ Server power supply
- ◆ Charger

## ■ General Description

OSG80R250xF use advanced GreenMOS™ technology to provide low  $R_{DS(ON)}$ , low gate charge, fast switching and excellent avalanche characteristics. This device is suitable for active power factor correction and switching mode power supply applications.

◆ $V_{DS, min@Tjmax}$	850 V
◆ $I_{D, pulse}$	51 A
◆ $R_{DS(ON), max @ V_{GS}=10 V}$	250 mΩ
◆ $Q_g$	41.2 nC

## ■ Schematic and Package Information

<b>Schematic Diagram</b> 	<b>Pin Assignment Top View</b> 
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## ■ Absolute Maximum Ratings at $T_j=25^{\circ}C$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	$V_{DS}$	800	V
Gate source voltage	$V_{GS}$	±30	V
Continuous drain current <sup>1)</sup> , $T_C=25^{\circ}C$	$I_D$	17	A
Continuous drain current <sup>1)</sup> , $T_C=100^{\circ}C$		10.8	
Pulsed drain current <sup>2)</sup> , $T_C=25^{\circ}C$	$I_{D, pulse}$	51	A
Power dissipation <sup>3)</sup> , $T_C=25^{\circ}C$	$P_D$	101	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	800	mJ
MOSFET dV/dt ruggedness, $V_{DS}=0...640 V$	dV/dt	50	V/ns
Reverse diode dV/dt, $V_{DS}=0...640 V, I_{SD} \leq I_D$	dV/dt	15	V/ns
Operation and storage temperature	$T_{stg}, T_j$	-55 to 150	°C

## ■ Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	1.24	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction-ambient <sup>4)</sup>	$R_{\theta JA}$	62.0	$^{\circ}\text{C}/\text{W}$

## ■ Electrical Characteristics at $T_j=25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	800			V	$V_{GS}=0\text{ V}, I_D=250\ \mu\text{A}$
		850				$V_{GS}=0\text{ V}, I_D=250\ \mu\text{A}, T_j=150^{\circ}\text{C}$
Gate threshold voltage	$V_{GS(th)}$	2.9		3.9	V	$V_{DS}=V_{GS}, I_D=250\ \mu\text{A}$
Drain-source on-state resistance	$R_{DS(on)}$		0.20	0.25	$\Omega$	$V_{GS}=10\text{ V}, I_D=8.5\text{ A}$
			0.44			$V_{GS}=10\text{ V}, I_D=8.5\text{ A}, T_j=150^{\circ}\text{C}$
Gate-source leakage current	$I_{GSS}$			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	$I_{DSS}$			10	$\mu\text{A}$	$V_{DS}=800\text{ V}, V_{GS}=0\text{ V}$

## ■ Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	$C_{iss}$		2425.4		pF	$V_{GS}=0\text{ V}, V_{DS}=50\text{ V}, f=100\text{ kHz}$
Output capacitance	$C_{oss}$		136.0		pF	
Reverse transfer capacitance	$C_{rss}$		3.0		pF	
Turn-on delay time	$t_{d(on)}$		32.6		ns	$V_{GS}=10\text{ V}, V_{DS}=400\text{ V}, R_G=2\ \Omega, I_D=8\text{ A}$
Rise time	$t_r$		15.9		ns	
Turn-off delay time	$t_{d(off)}$		70.2		ns	
Fall time	$t_f$		6.9		ns	

## ■ Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	$Q_g$		41.2		nC	$I_D=8\text{ A}$ , $V_{DS}=400\text{ V}$ , $V_{GS}=10\text{ V}$
Gate-source charge	$Q_{gs}$		10.8		nC	
Gate-drain charge	$Q_{gd}$		12.4		nC	
Gate plateau voltage	$V_{\text{plateau}}$		5.4		V	

## ■ Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward current	$I_S$			17	A	$V_{GS} < V_{th}$
Pulsed source current	$I_{SP}$			51		
Diode forward voltage	$V_{SD}$			1.3	V	$I_S=17\text{ A}$ , $V_{GS}=0\text{ V}$
Reverse recovery time	$t_{rr}$		356.0		ns	$V_R=400\text{ V}$ , $I_S=8\text{ A}$ , $di/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge	$Q_{rr}$		5.2		$\mu\text{C}$	
Peak reverse recovery current	$I_{rrm}$		28.0		A	

## ■ Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3)  $P_d$  is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_a=25\text{ }^\circ\text{C}$ .
- 5)  $V_{DD}=50\text{ V}$ ,  $R_G=50\text{ }\Omega$ ,  $L=60\text{ mH}$ , starting  $T_j=25\text{ }^\circ\text{C}$ .

■ **Electrical Characteristics Diagrams**

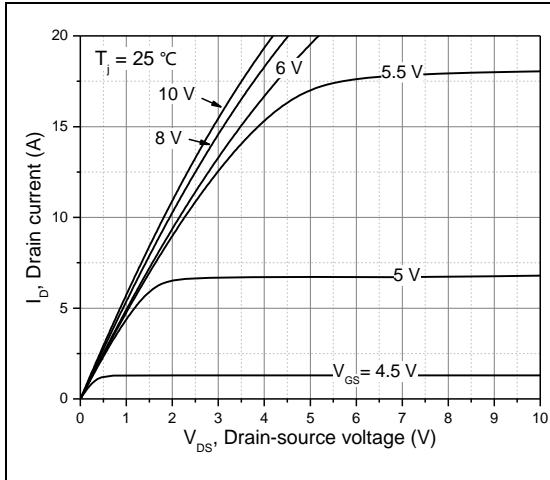


Figure 1, Typ. output characteristics

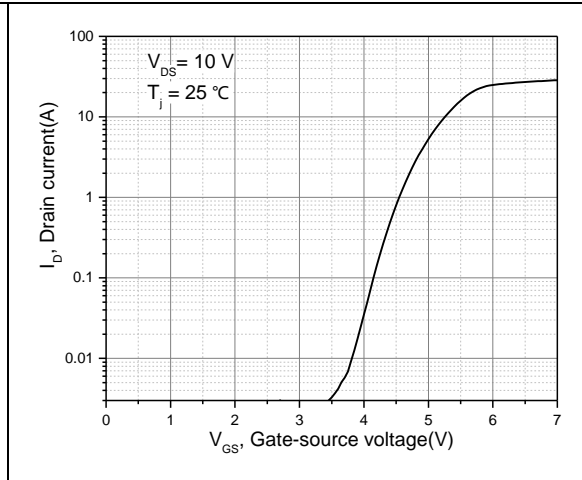


Figure 2, Typ. transfer characteristics

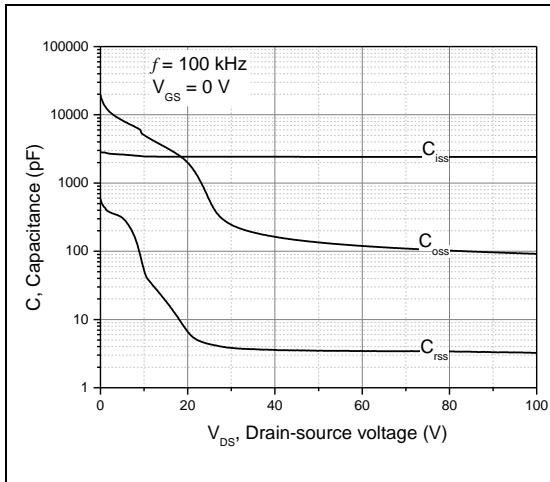


Figure 3, Typ. capacitances

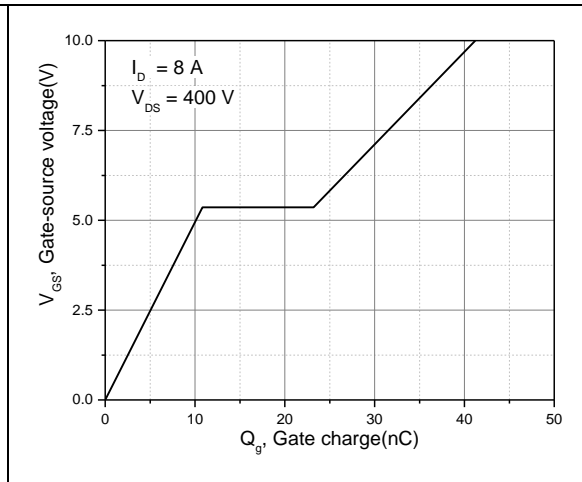


Figure 4, Typ. gate charge

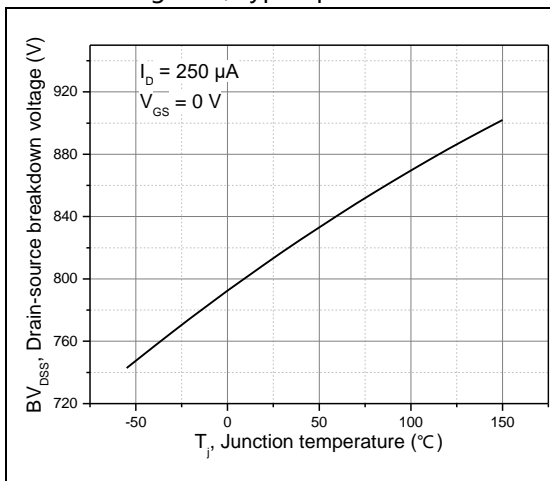


Figure 5, Drain-source breakdown voltage

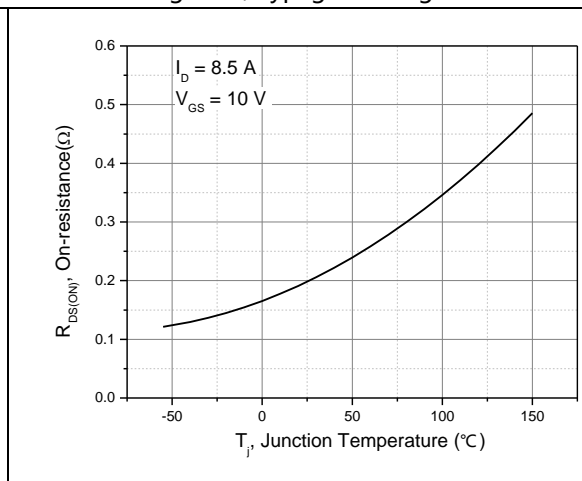


Figure 6, Drain-source on-state resistance

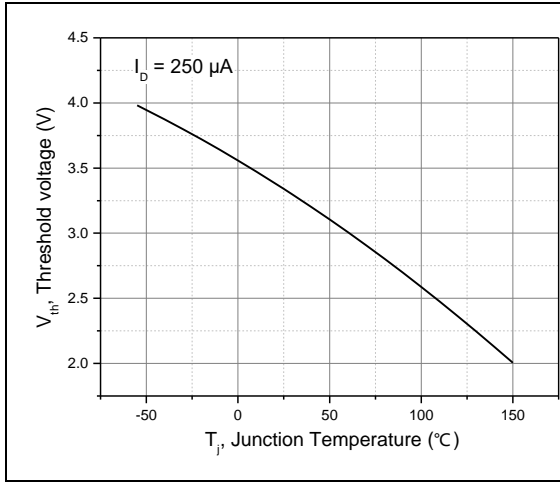


Figure 7, Threshold voltage

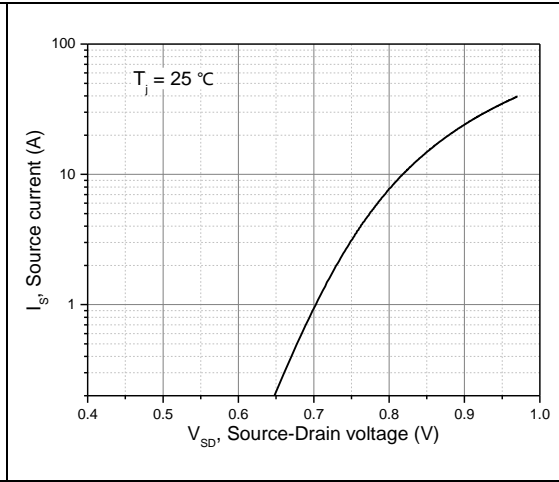


Figure 8, Forward characteristic of body diode

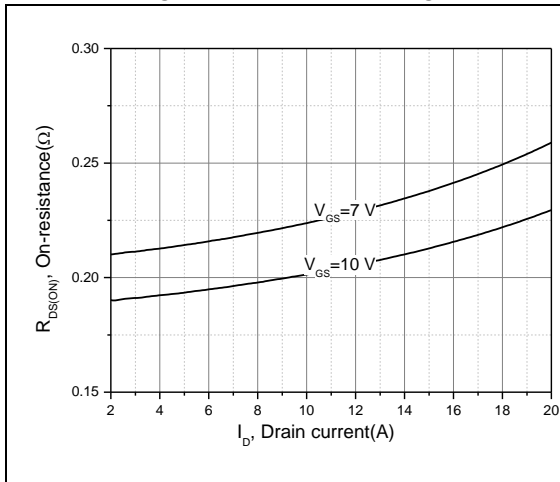


Figure 9, Drain-source on-state resistance

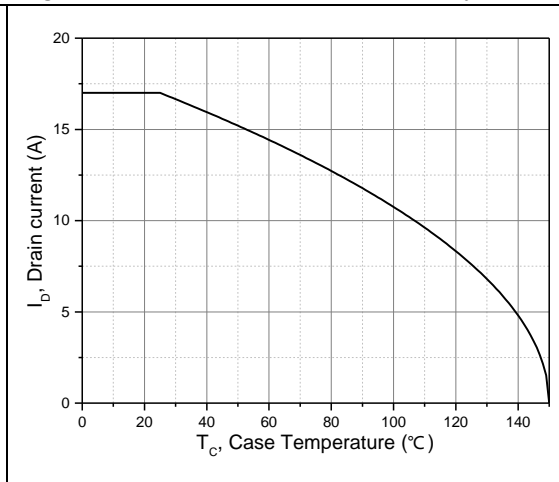


Figure 10, Drain current

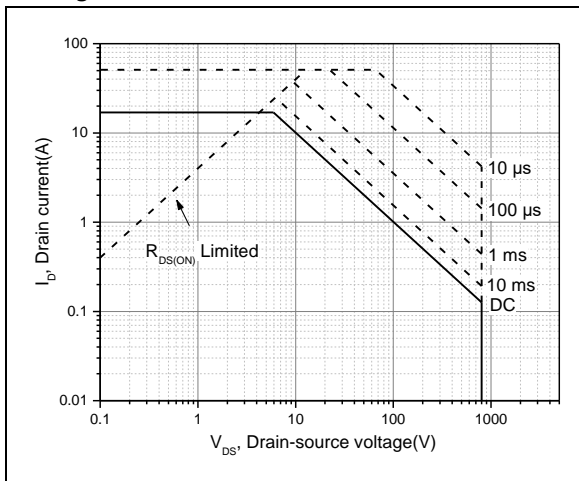


Figure 11, Safe operation area  $T_C=25\text{ }^\circ\text{C}$

■ Test circuits and waveforms

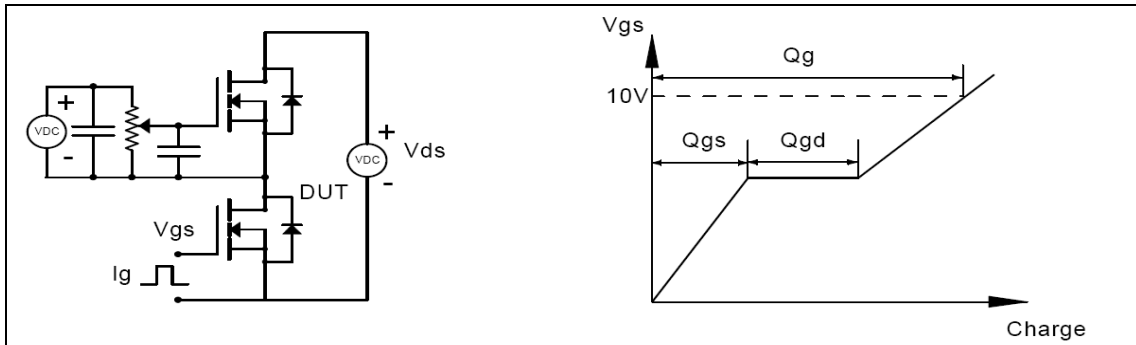


Figure 1, Gate charge test circuit & waveform

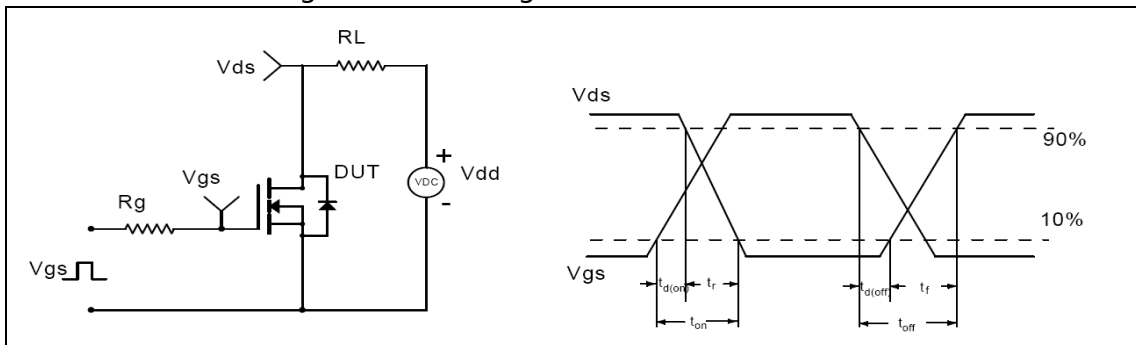


Figure 2, Switching time test circuit & waveforms

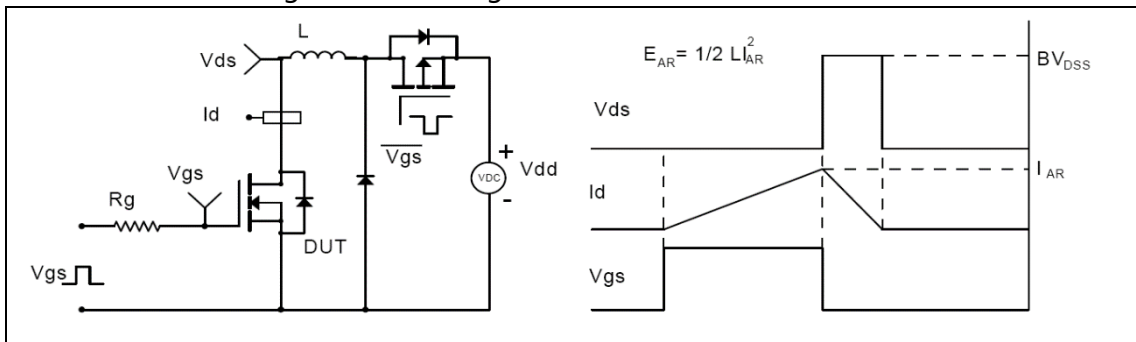


Figure 3, Unclamped inductive switching (UIS) test circuit & waveforms

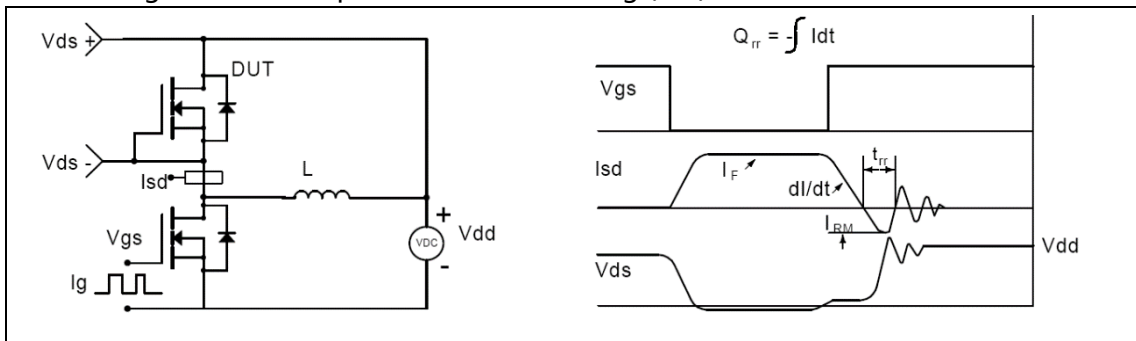
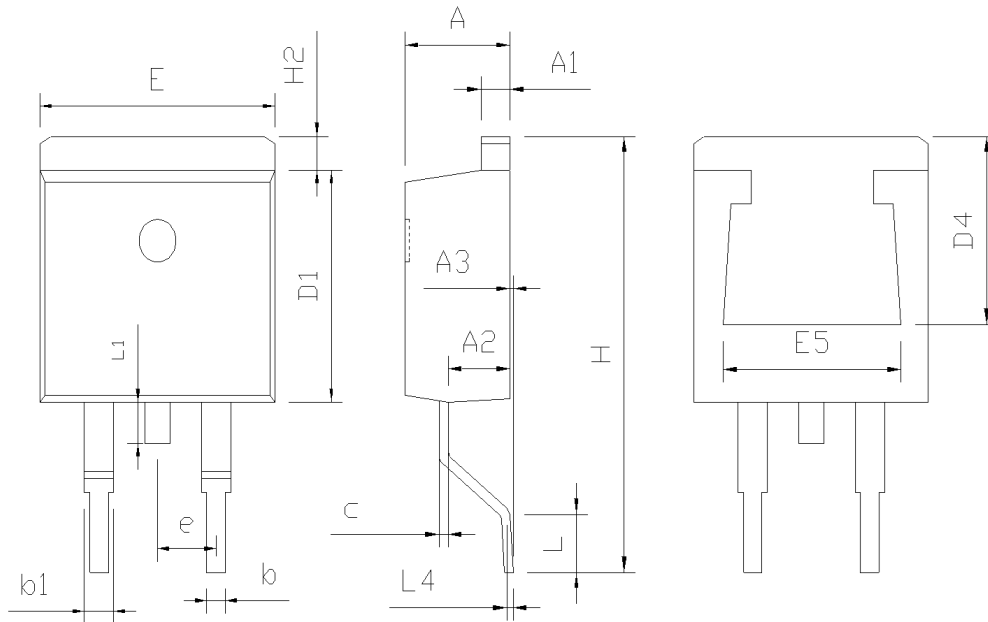


Figure 4, Diode reverse recovery test circuit & waveforms

**■ Package Information**

Figure1, TO263 package outline dimension

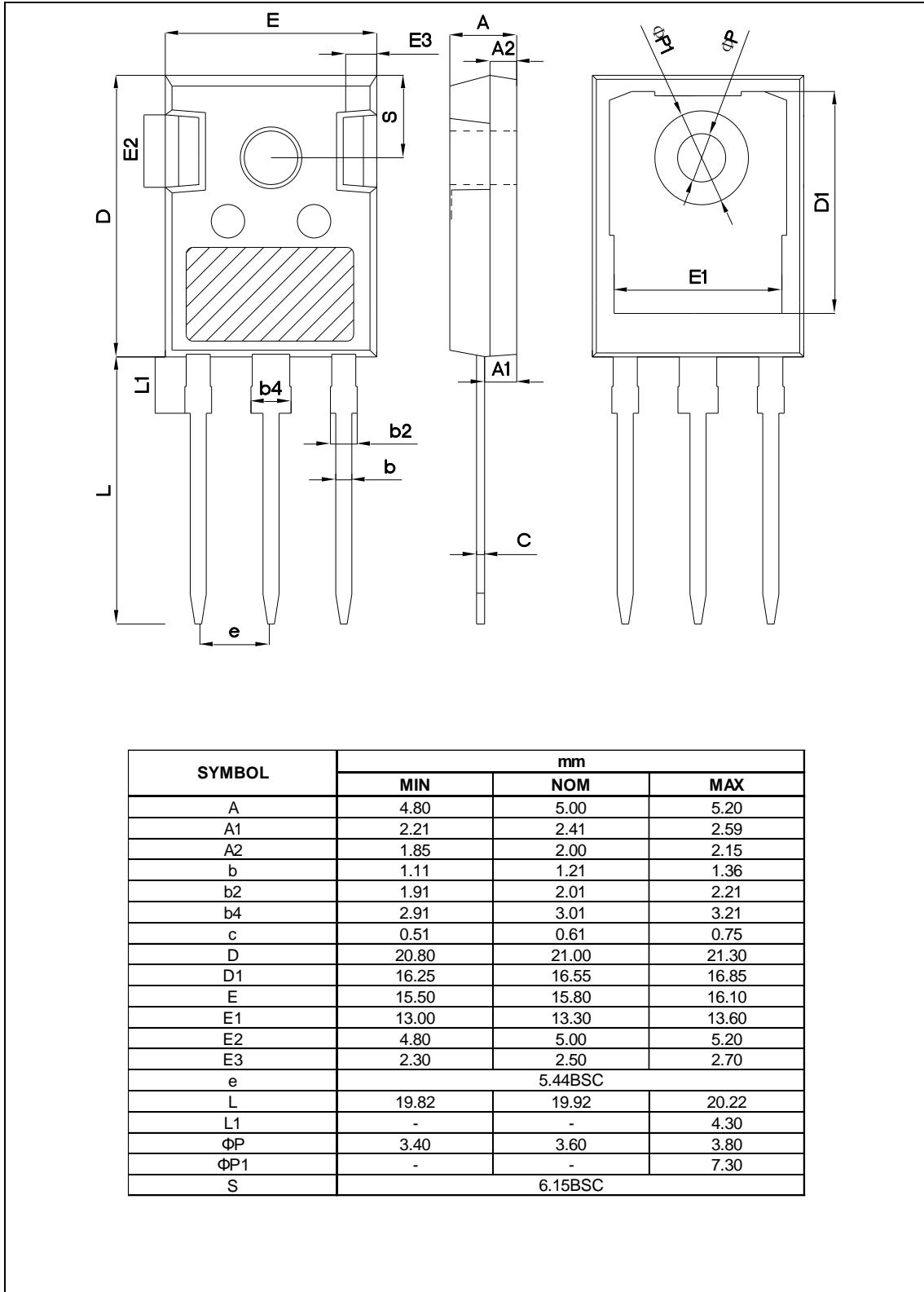


SYMBOL	mm		
	MIN	NOM	MAX
A	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
A3	0.00	0.13	0.25
b	0.70	0.81	0.96
b1	1.17	1.27	1.47
c	0.30	0.38	0.53
D1	8.50	8.70	8.90
D4	6.60	-	-
E	9.86	10.16	10.36
E5	7.06	-	-
e	2.54 BSC		
H	14.70	15.10	15.50
H2	1.07	1.27	1.47
L	2.00	2.30	2.60
L1	1.40	1.55	1.70
L4	0.25 BSC		



**■ Package Information**

Figure2, TO247 package outline dimension



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**■ Ordering Information**

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Package	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Box/Carton Box	Units/Carton Box
TO263	50	20	1000	6	6000
TO247	30	11	330	6	1980

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**■ Product Information**

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Product	Package	Pb Free	RoHS	Halogen Free
OSG80R250KF	TO263	yes	yes	yes
OSG80R250HF	TO247	yes	yes	yes